

FIG. 1

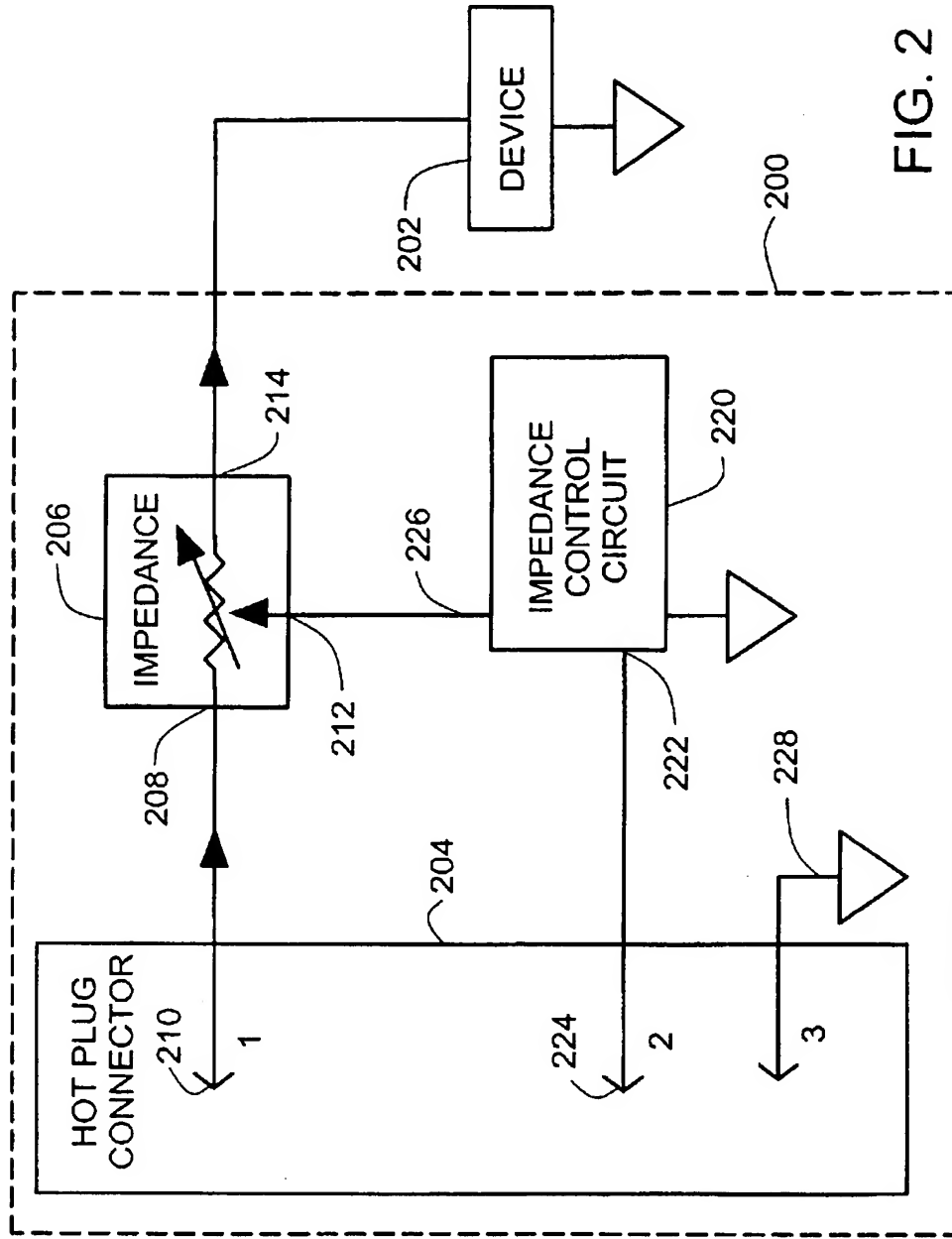


FIG. 2

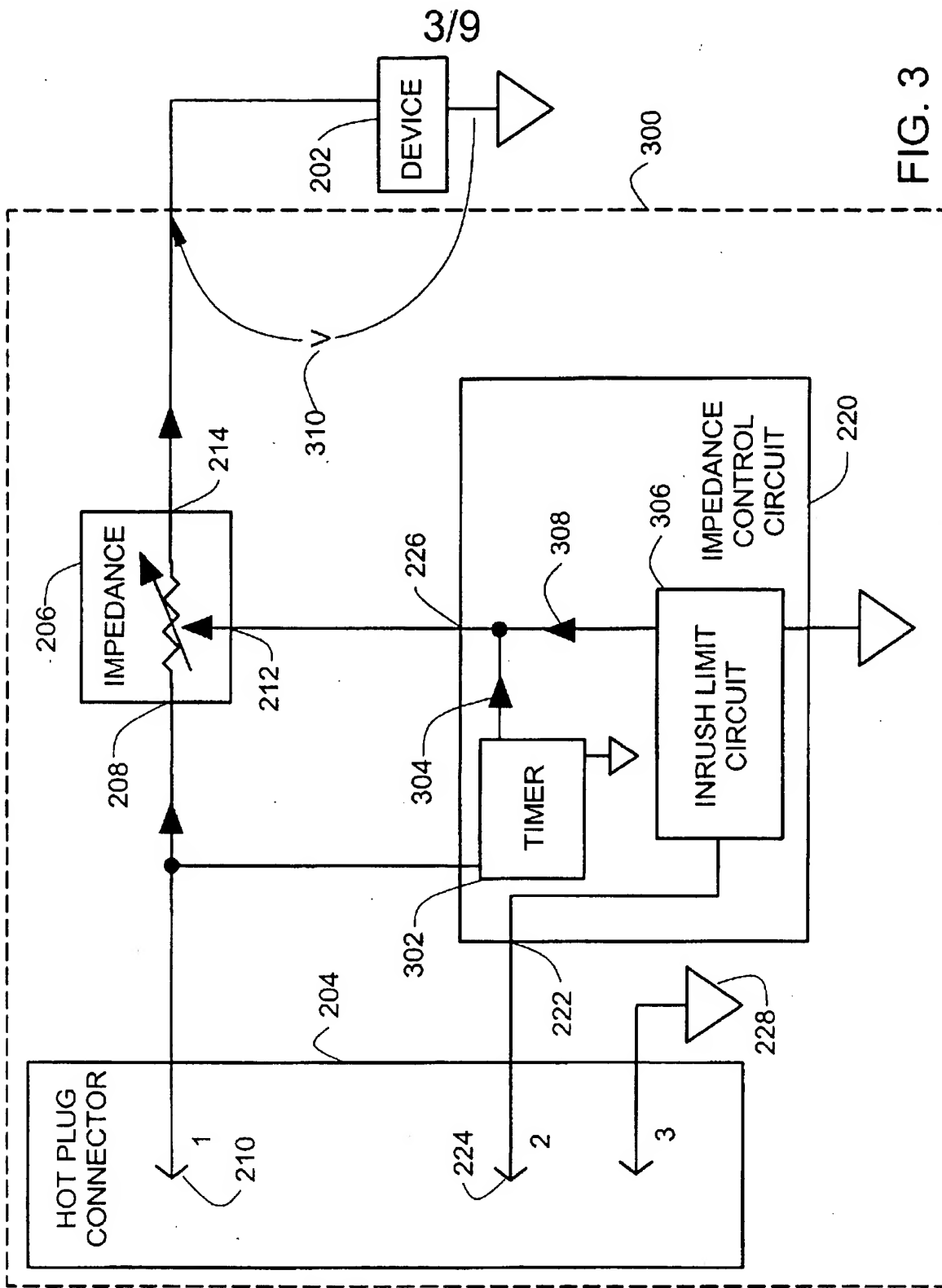


FIG. 3

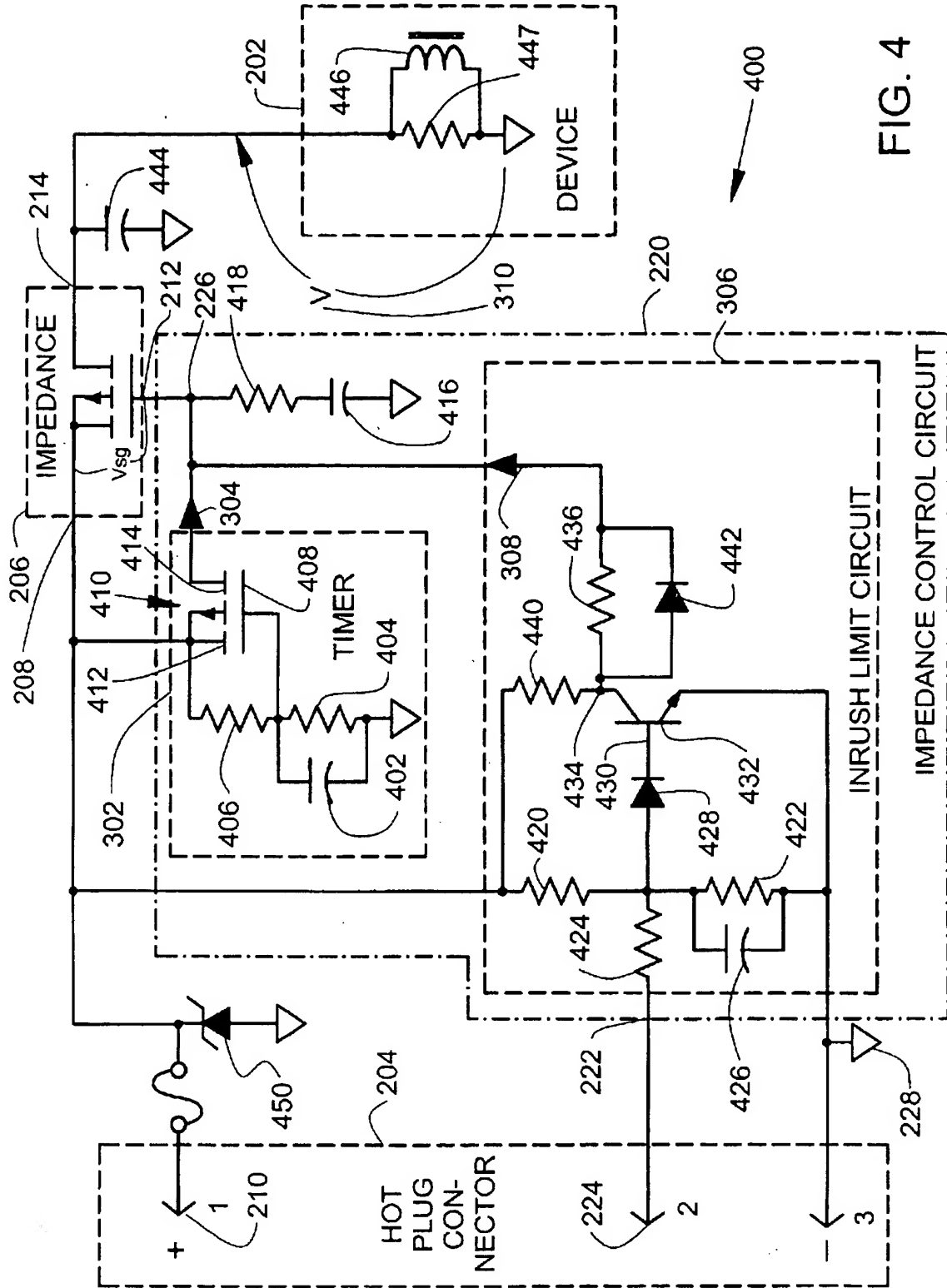


FIG. 4

KEY	
-----	DEVICE VOLTAGE
.....	IMPEDANCE CONTROL INPUT
----	LOGIC INPUT
—	INPUT CURRENT

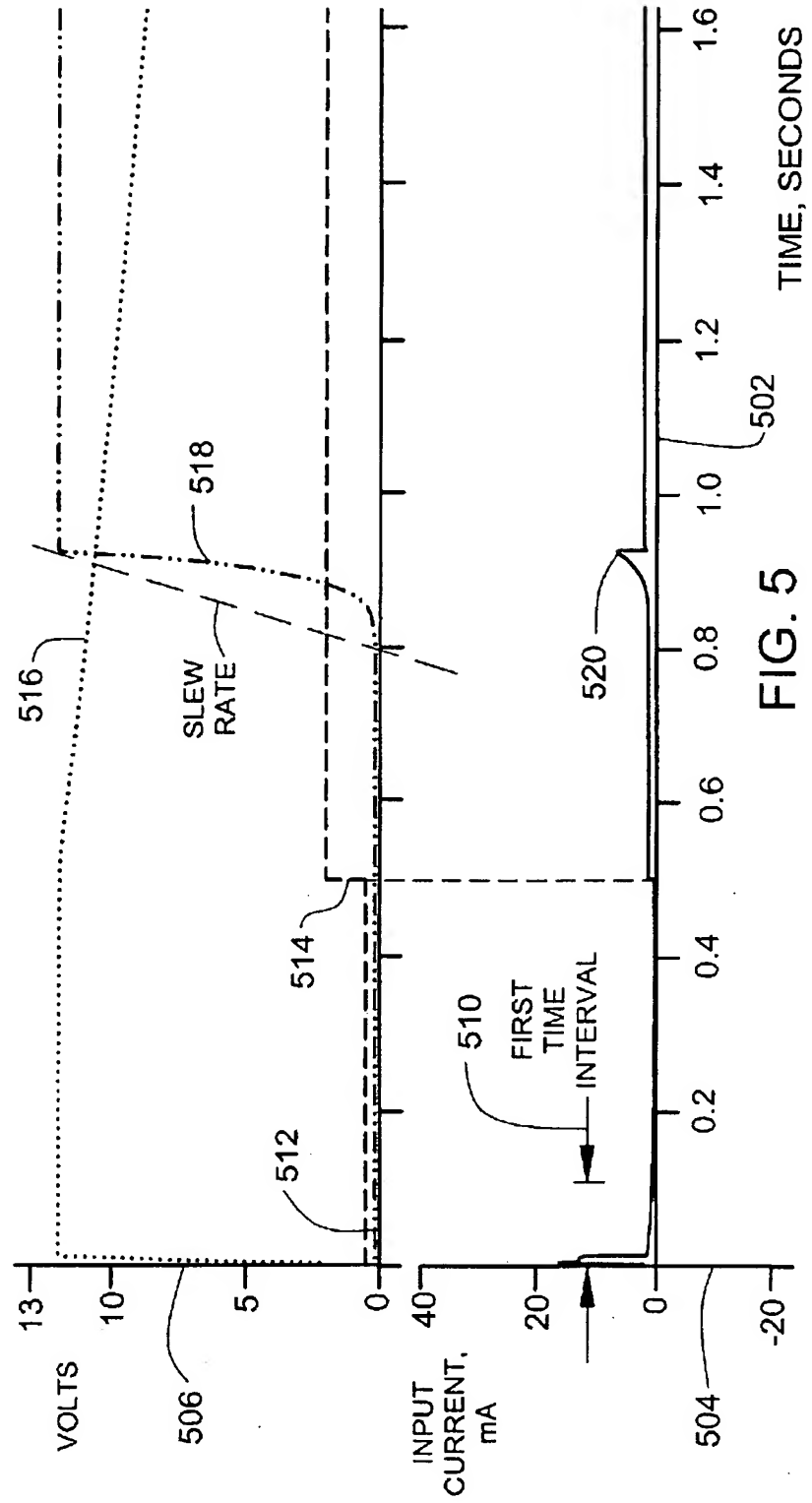


FIG. 5

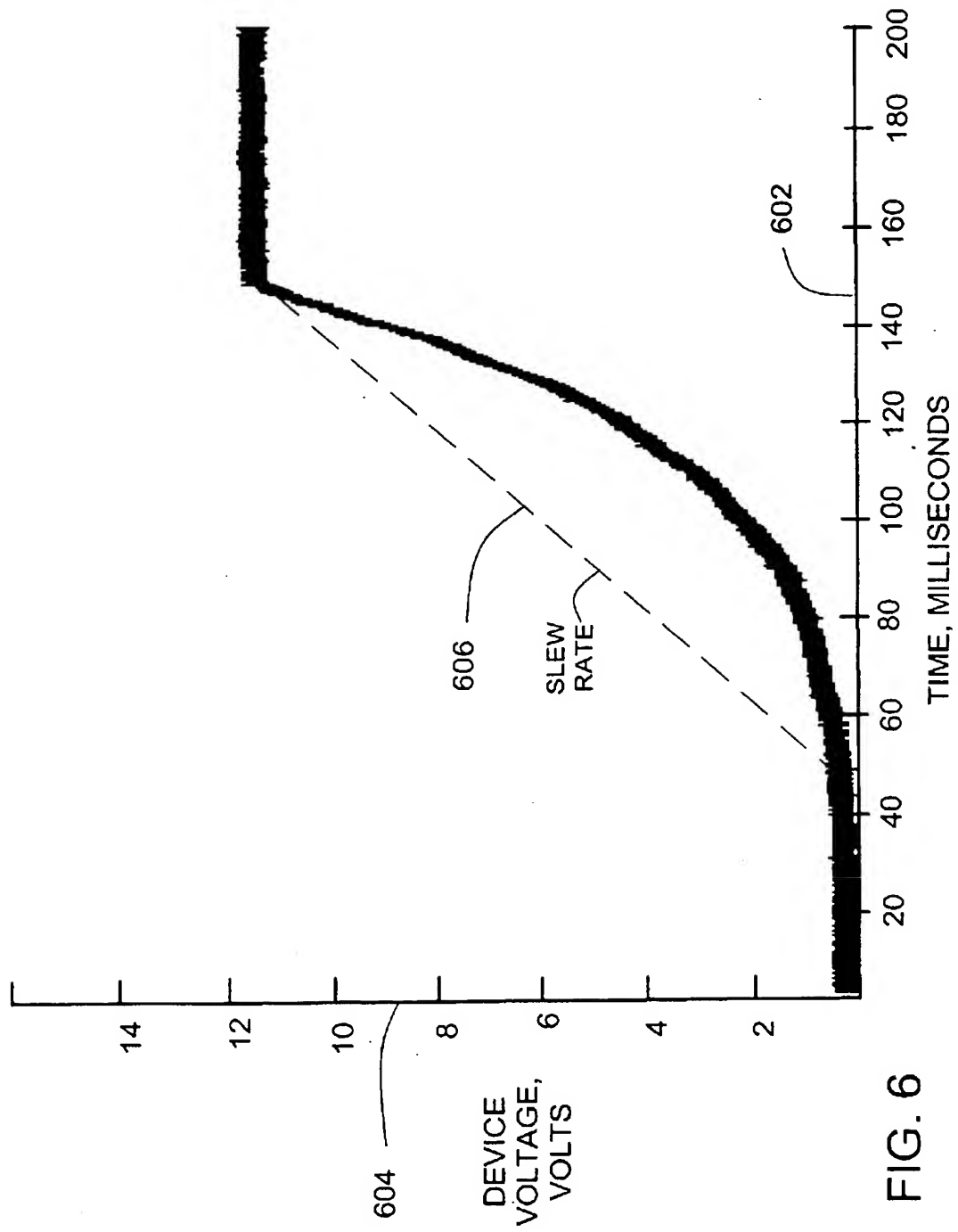


FIG. 6

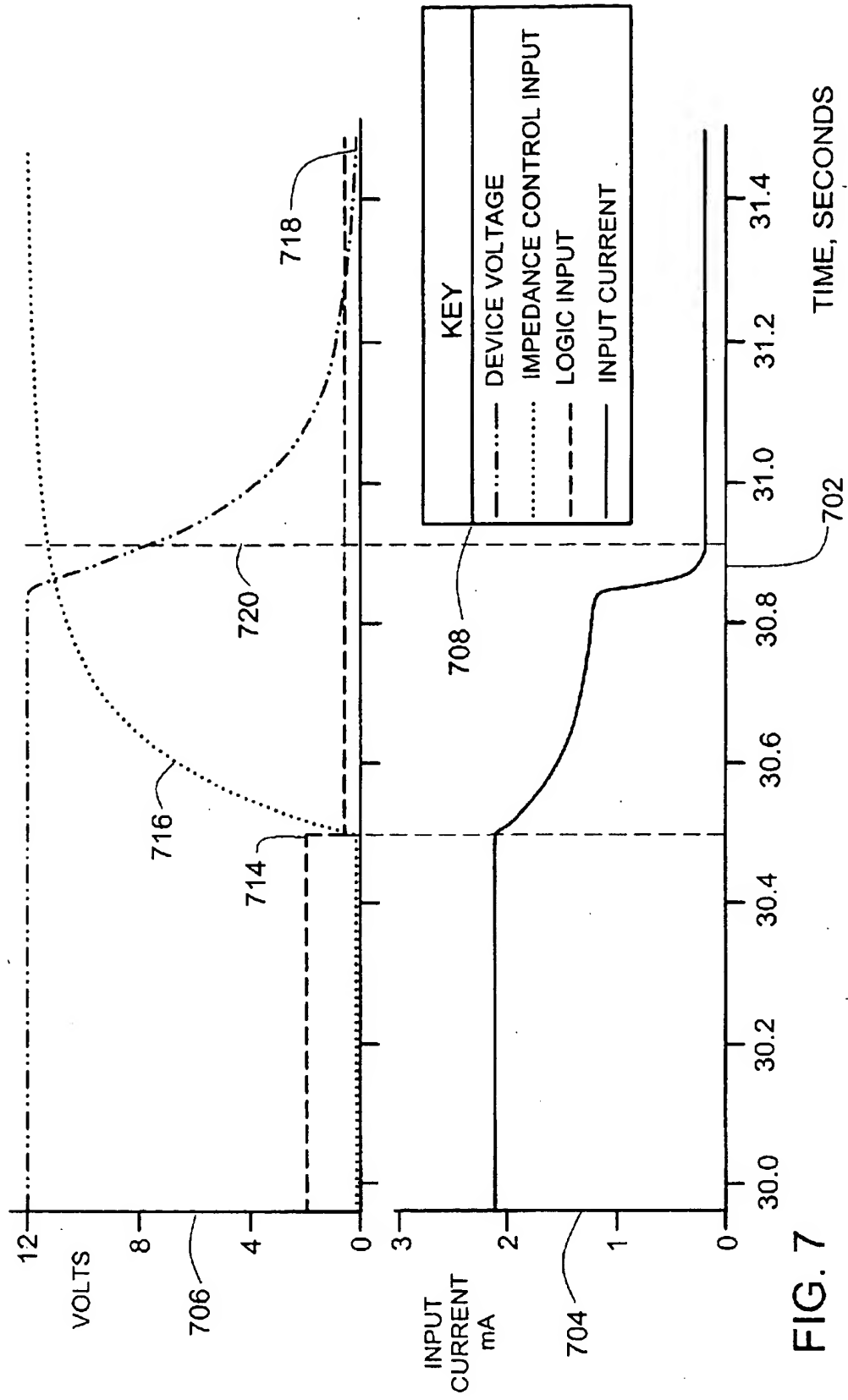


FIG. 7

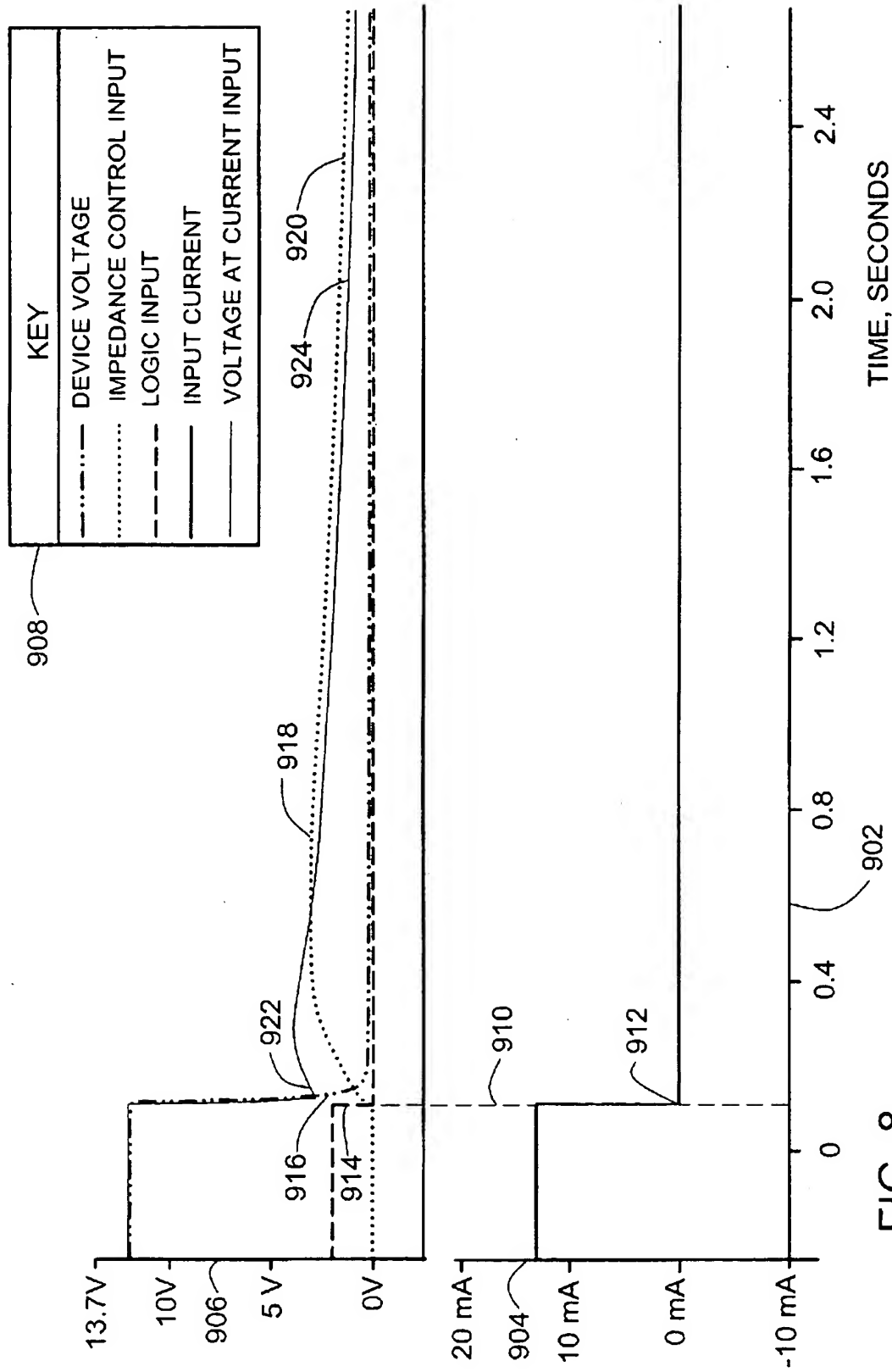


FIG. 8

